

**Quiz (8)**

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Time duration: 8 minutes

Specify the size of a ROM (number of words and number of bits per words) that will accommodate the truth table for the following combinational circuit:

- a. A binary multiplier that multiplies 4-bit numbers.

$2^8$  words x 8-bit

(3)

- b. A 4-bit adder-subtractor.

$2^8$  words x 5-bit

(3)

- c. A BCD-to-seven segment decoder.

$2^4$  words x 7-bit

(4)